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	Applicant Number	10/840,015	
INFORMATION DISCLOSURE	Filing Date	May 5, 2004	
TATEMENT BY APPLICANT	First Named Inventor	Adrian J. Drexler et al.	
	Art Unit	2631- 2866	
	Examiner Name	Not Yet Assigned To an	
0 6 2015 (Use as many sheets as necessary)	Attorney Docket Number	MIC-44	
(Use as many sneets as necessary)	Confirmation Number	9647	

1 of 2

	U. S. PATENT DOCUMENT					
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (if known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (if known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Examiner Signature	Date Considered #13 05

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Applicant Number 10/840,015 **Filing Date** May 5, 2004 **First Named Inventor** Adrian J. Drexler et al. **Art Unit** 2631 28/6 **Examiner Name** Net Yet Assigned **Attorney Docket Number MIC-44 Confirmation Number** 9647

Date

Considered

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(Use as many sheets as necessary)

Sheet 2 of 2

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r	Kim et al., "A Low-Jitter Mixed-Mode DLL for High-Speed DRAM Applications," IEEE Journal of Solid-State Circuits, Vol. 35 No. 10, pp. 1430-1436, (2000).
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	Applicant Number	10/840,015	
INFORMATION DISCLOSURE	Filing Date	May 5, 2004	
SEP 2 Was many sheets as necessary)	First Named Inventor	Adrian J. Drexler et al.	
	Art Unit	2816	
	Examiner Name	Tuan Thieu Lam	
	Attorney Docket Number	MIC-44	
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	U. S. PATENT DOCUMENT					
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (d known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant	
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62	Examiner Signature	Mula	Date Considered	11/3/05

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